

C L A I M S

1. A method for debugging chipcard applications comprising:
 using a chipcard application /terminal application
 standard communication protocol for transporting business
 commands of a terminal application to a chipcard application
 and debug information of a debug control program to the
 chipcard application; and
 evaluating the business commands and debug information in
 a module layered between between the chipcard application and
 the terminal application, and between the chipcard application
 and the debug control program.
2. The method of claim 1 in which said debug information is
transported within Application Protocol Data Units (APDUs).
3. The method of claim 1 applied to JAVA cards, and filesystem
oriented chipcards.
4. The method of claim 1 wherein evaluating the business
commands and debug information further comprises determining
whether an incoming command is an incoming debug instruction.
5. The method of claim 4 further comprising:
 sending the incoming command to the chipcard application;
 receiving a response from the chipcard application; and
 sending the response to the debug control program if the
incoming command was determined to be an incoming debug
instruction.
6. The method of claim 4 further comprising:
 sending the incoming command to the chipcard application;
 receiving a response from the chipcard application; and
 sending the response to the terminal application if the
incoming command was determined not to be an incoming debug
instruction.

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7. A method for debugging chipcard applications comprising:
evaluating debug control information to distinguish between
debug information input and business information input to a
chipcard application;

sending, based upon the evaluation, debug information output
to a debug control program and business information output to
a terminal application.

8. A computer program module having computer readable
instruction means on a computer readable medium, comprising:

instruction means enabling an interface for input to and
output from a chipcard driver; and

instruction means enabling an evaluation of debug control
information for distinguishing between debug information input
and business information input to the chipcard application.

9. The computer program module of claim 8 further comprising
instructions means for feeding, dependent upon an evaluation
result,

i) an output response, from a chipcard application, of debug
information to a debug control program; and

i) business information output, issued from the chipcard
application, to a terminal application.

10. The computer program module of claim 9 wherein the
computer program module resides in a chipcard driver program.

11. A chip card driver comprising:

means for enabling an evaluation of debug control
information for distinguishing between debug information input
and business information input to a chipcard application;

means for sending, based upon the evaluation, debug
information output to a debug control program and business
information output to a terminal application.

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12. A data processing system having a processor and memory, comprising:

an interface module having means for interfacing between a chipcard application and a debug control program and between the chipcard application and a terminal application;

means for enabling an evaluation of debug control information for distinguishing between debug information input and business information input to a chipcard application; and

means for sending, based upon the evaluation, debug information output to a debug control program and business information output to a terminal application.

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